

SVERI'S COLLEGE OF ENGINEERING (POLYTECHNIC), PANDHARPUR.

Department of Electrical Engineering

Issue 8



Editor **Prof.Korade H.D.**

Faculty Coordinator **Prof. Pawar G.H.**

Enlightening the World with Electricity
Student Coordinator
Ms.Salgar K.D.

❖ Vision of Institute

To be recognized as an excellent diploma Institute in Maharashtra for providing technical education with focus on various skill sets enabling to reach higher goals in the field

❖ Mission of Institute

To impart value based technical education by inculcating technical knowledge, skills and acumens

***** Vision of Electrical Engineering Department

To provide diploma education strengthened with basic knowledge and skills along with professional ethics enabling students to reach higher goals in Electrical Engineering

❖ Mission of Electrical Engineering Department

To impart value based technical education enriched by knowledge, professional ethics and skills in Electrical Engineering

Program Education Objectives of Electrical Engineering Department

PEO 1: Apply technical knowledge in their professional career for solving contemporary problems in the field of Electrical Engineering.

PEO 2: Demonstrate leadership, teamwork, commitment and other ethics in professional career.

PEO 3: Demonstrate strong communicative abilities in their professional career.

PEO 4: Engage in lifelong learning for professional and personal advancement.

❖ Views of HOD

I welcome you all for 1st semester of the academic year 2018-19.Last semester was full of departmental events like Quiz Competition, Poster Presentation, industrial visit, guest lecture, workshop and training etc. This issue of Ignite has given Focus on student's academic growth and participation in different events.

"BE A BRIGHT SPARK LIGHTS OFF TILL ITS DARK!"

Prof. H.D. Korade

Departmental AchievementsSpecial Achievements:

Summer 2018 Result of FYEE

Sr.no.	Student Name	%Marks
01	Mr.Gade Rahul Hanumant	97.13
02	Ms.Ritapure Vaishnavi Vidyadhar	93.88
03	Ms.Kokare Kajal Maruti	91.38

Summer 2018 Result of SYEE

Sr.no.	Student Name	%Marks
01	Ms.Salagar Komal Digambar	91.88
02	Mr.Bansode Girish Mahendra	90.94
03	Ms.Shendage Swapnali Vikas	90.35

Summer 2018 Result of TYEE

Sr.no.	Student Name	%Marks
01	Ms.Pore Shraddha Vishnu	90.18
02	Ms.Gangthade Swati S.	89.03
03	Ms.Sawant Kishori Santosh	88.91

100 out of 100 scored in Applied Mathematics

Sr.no.	Student Name	
01	Mr.Gade Rahul Hanumant	
02	Ms.Ritapure Vaishnavi Vidyadhar	

50 out of 50 scored in Elements of Mechanical Engineering

Sr.no	Student Name
01	Ms.Attar Tasmiya Muktar

Students got above 97 marks

Sr.no.	Student Name	Subject
		Fundamentals of
1		Electrical
		Engineering
	Ms.Ritapure Vaishnavi	Fundamentals of
2	Vidyadhar	Electrical
		Engineering
2	Ms.Ritapure Vaishnavi	Elements of
3	Vidyadhar	Electronics

Technical Event

Department of Electrical Engineering has organized **Poster presentation** and **Quiz Competition** under "National Level Talent Hunt" with the objective of this event was to provide opportunities for students from all over the State to exchange and evaluate viewpoints regarding social issues.



Industrial Visit Planning During 2018-19

To make student aware of growing and changing needs of industry and to get more practical knowledge, we were planned industrial visits to various companies and research centre for our students

SY Student visit:

- 1) Samarth Electricals, Pandharpur.
- 2) Kothari Cables ,Solapur.
- 3) Windmill Power Generation, Bhivghat.

TY Student visit:

- 1) LHP, Solapur.
- 2) Lokmangal Sugar Pvt Ltd. Solapur.
- 3) L&T Switchgear, Pune.
- 4) Railway Signaling system, Kalyan.

Guest Lectures

For professional development of student, we have invited experts from industry and other reputational institutes to guide students.

Under this scheme during this semester following experts were invited.

- 1) Guest Lecture on "Modern Electrical Trends In Electrical Engineering" by Mr. Akireddy Shravankumar for TY students.
- 2) Guest Lecture on "Switch gear and Protection" by Mr. Linge Prashant for TY students.
- 3) Guest Lecture on "Transformer Manufacturing" by Mr. Kulkarni Nachiket for SY students

Workshop and Training

Department of Electrical Engineering has organized **Two Week Workshop and Training** on **"Transformer Manufacturing"** for students to get more practical knowledge and working experience in industry.





ArticleSmart Grid and Digital Substation

The data acquisition in electric power system is very important and includes a lot of areas such as substation. Digital substation is one of the key parts of smart grid and the network of process layer is an important foundation for smart substation which is related to the reliability and real-time of data acquisition and switch control.

The type of message of process layer which include GOOSE (General Object-Oriented Substation Event), MSV (Multiple Sample Value) and synchronization with network will be the content of data acquisition in this technical article.

As the trend of development about the power system is along the large capacity, high voltage, extra-high voltage and the same about the device is along the small, smart and high-reliability, the electric transformer is widely used for its many advantages such



as small size, light weight, good capability of antielectromagnetic, wide dynamic range which is not easy to saturation, simple insulation structure and easy to the transmission of digital signal, etc.

Developments in communication have done much to realize the digital substation, but to realize a full digital substation it is necessary to have everything in digital form. Whilst much substation protection, control, and automation technology has always been digital (trip signals, interlocking signals, etc.), the principal power system inputs of voltage and current have traditionally been presented in analogue form.

It has been traditional to present scaled versions of power system currents and voltages to measuring devices, protective relays etc. Scaled versions can easily be produced using conventional electrical transformers, although capacitor dividers may be additionally employed for transforming very high voltages.



Editorial:

It gives me great pleasure to present 8th volume of our departmental news letter *IGNITE* to you which us gives us the opportunity to see the achievements of our Department. In this issue we cover the different task carried out at Electrical Engineering Department training programs, Project etc. We are thankful to all faculty and students who have give their contribution for preparing this news letter. We attempted best and given best efforts.

"We Play With Things, Others Fear to Touch...."

Prof. G.H.Pawar





